gsbuser@gsbm1:
DIR:/mnt/code/gsbuser/source_code/dvl.6/realtime/LATEST_jroy/
different code directory and the details:

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fixed_pt_delay_released_2pols_32MHz_quadonly_beam.xnet
    == 32 MHz Total intensity + Beam with xnet.

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fixed_pt_delay_released_2pols_phasSHM_latest_fullpol.xnet
    == 16 MHz Full Polariztion + Beam with xnet.

----

gsb32_rawdump_splittedSHM_daschain
    == ?? (Jitendra/NIlesh rawdump corr-> interface with ONLINE, fully done??)

----

fixed_pt_delay_released_2pols_phasSHM_latest_acqpsr_1integ
    == 16 MHz 2 pols. acqpsr with 1integ (post_int)

----

fixed_pt_delay_released_2pols_phasSHM_latest.htvis
    == 16 MHz 2 pols with high time resolution visibility.
        125 mSec 256 channels. (visibility comes through gsbm2 -> gsbm4

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fixed_pt_delay_released_2pols_phasSHM_latest.bak
    == 16 MHz 2 pols (Backup)

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fixed_pt_delay_released_2pols_16MHz.xnet_beam_header
    == 16 MHz 2 pols with xnet with beam_header

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fixed_pt_delay_released_2pols_phasSHM_latest_fullpol
    == 16 MHz 4 pols

----

fixed_pt_delay_released_2pols_16MHz.xnet
    == 16 MHz 2 pols with xnet

----

fixed_pt_delay_released_2pols_32MHz_quad_dual_beam.512.xnet
    == 32 MHz full stokes 512 channels with xnet with beam

----

fixed_pt_delay_released_2pols_32MHz_quadonly_beam_gps_TS_DUT1
    == 32 MHz 2 pols beam gps timestamp, DUT1 correction on (for sample,
        but not implemented in any default GSB code)

----

fixed_pt_delay_released_2pols_phasSHM_latest
    == 16 MHz 2 pols (all channesl combinations 512 to 32 are available
        as compile time option. Use compile script which invoking
        makefile with channels as argv.

----

fixed_pt_delay_released_2pols_32MHz_quadonly_beam
    == 32 MHz 2 pols /total intensity

----

fixed_pt_delay_released_2pols_32MHz_quadonly_beam.bak
    == 32 MHz 2 pols (Backup)

----

fixed_pt_delay_released_2pols_32MHz_quad_dual_beam.256.xnet
    == 32 MHz full stokes 256 channels with xnet

----
fixed_pt_delay_released_2pols_32MHz_quad_dual_beam
    == 32 MHz full stokes 256/512 channels (original code from where
        256/512 channels were started, currently not valid.)
----
fixed_pt_delay_released_2pols_phasSHM_latest_bandpass_test
    == 16 MHz 2 stokes: Decimated code. It's compilation at execution
        time. Decimation available from 4 to 128. Basically 16 MHz
decimated. 512 and 256 channels option available at compile time.
----
fixed_pt_delay_released_2pols_32MHz_quad_dual_beam.256.xnet.ssk.31jan2014
    == 32 MHz full stokes 256 channels with xnet, under test for xnet failure
----
fixed_pt_delay_released_2pols_32MHz_quad_dual_beam.256.xnet_bypassed
    == 32 MHz Full stokes 256 channeles, x-net bypassed. Beam Tx in .14
     & .15 net.