**Project : System: Sub-system:**

**Unit Code: Rev. No: Date:**

Photograph of the unit with lids closed

 (lids not applicable in case of PCB alone)

**Photograph of the unit with lid closed**

**Project : System: Sub-system:**

**Unit Code: Rev. No: Date:**

 Photograph of the unit with lids open (Top View)

 (lids not applicable in case of PCB alone)

 Photograph of the unit with lids open (Bottom View)

 (lids not applicable in case of PCB alone)

**Circuit Diagram (Schematic):**

**Project : System: Sub-system:**

**Unit Code: Rev. No: Date:**

**Project : System: Sub-system:**

**Unit Code: Rev. No: Date:**

**Bill of Materials**

1. **Parts List**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Circuit | Package | Type/ | Tolerance | Ratings | Procurement information | Manufacturer/Vendor | Remarks |
| symbol | style | Value |   |   |   |   |   |
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**Project : System: Sub-system:**

**Unit Code: Rev. No: Date:**

**Bill of Materials**

1. **Materials List**
2. Substrate material: Details, manufacturer, size and conductor plating information.
3. Connectors: Type, Part No., Manufacturer and quantity
4. Fasteners, nuts, washers: Material, size and quantity
5. Solder lead: Details
6. Chassis: Material, Size, type, surface finish and drawing. (Drawing must be attached.)

**Project : System: Sub-system:**

**Unit Code: Rev. No: Date:**

**Circuit Layout**

Layout file details (like filename, folder etc)

Copy of the layout (top, bottom and drill details): Attach hard copy

Component placement diagram

**Project : System: Sub-system:**

**Unit Code: Rev. No: Date:**

**Test and Evaluation**

Unit Serial No:

Test Plots: Hard copies to be attached

Test results in tabular format (e.g: parameters, specifications and results)

Signature

Date: